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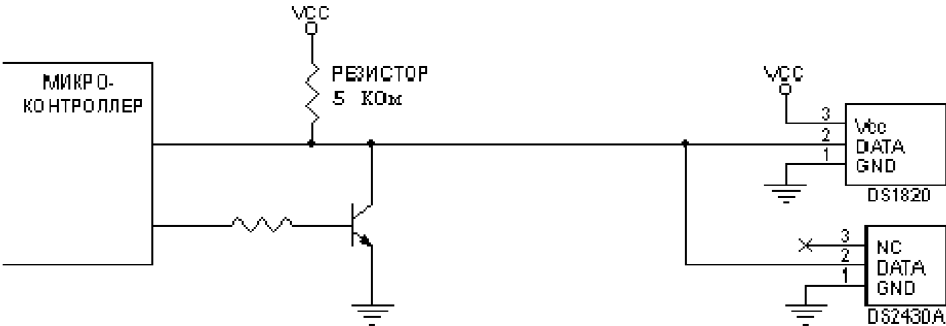
MicroLAN. / 0.8  
2.8 ... 6.0 2.2 MicroLAN

16300 bps  
7

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8051, RS232, 1.8 MicroLAN.

Схема подключения ведущего микроконтроллера к ведомым



2e56 MicroLAN  
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75

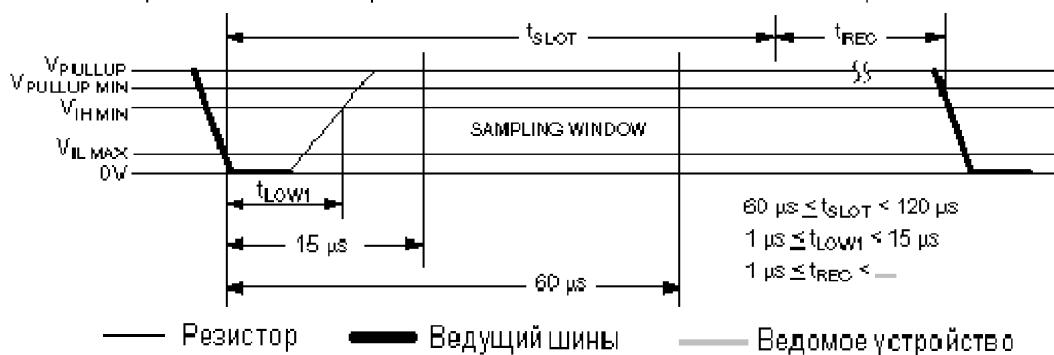
# MicroLAN

1-

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Рис. 1. Временная диаграмма записи логической единицы



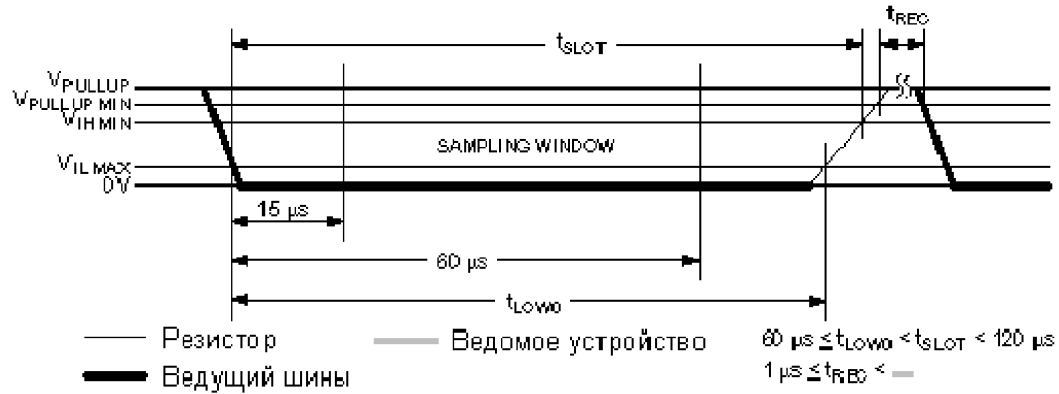
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0 (t<sub>LOW0</sub>) 1 (t<sub>LOW1</sub>) 15 60 ; , 60 .

(480 )

120

Рис. 2. Временная диаграмма записи логического нуля



120 , 0

t<sub>REC</sub> 1 ,

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0 1.

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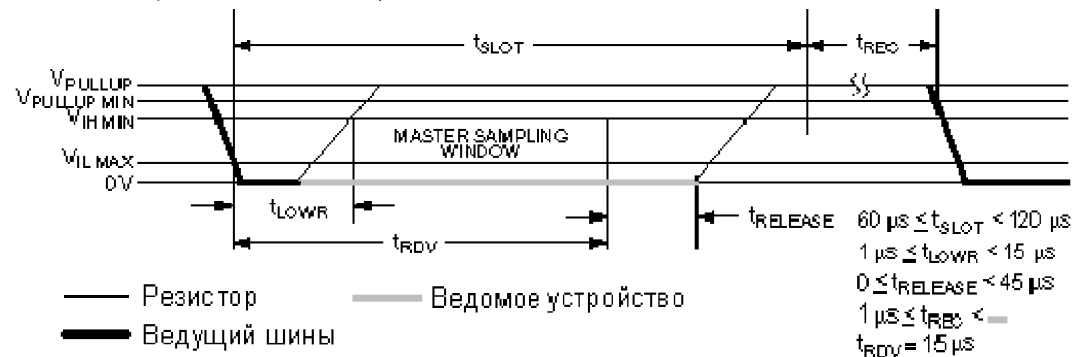
1,

0, t<sub>RDV</sub>,

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15 .

Рис. 3. Временная диаграмма чтения



t<sub>LOW0</sub> 1 ,

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t<sub>RDV</sub> 1-

t<sub>RELEASE</sub> 0 45 ,

15

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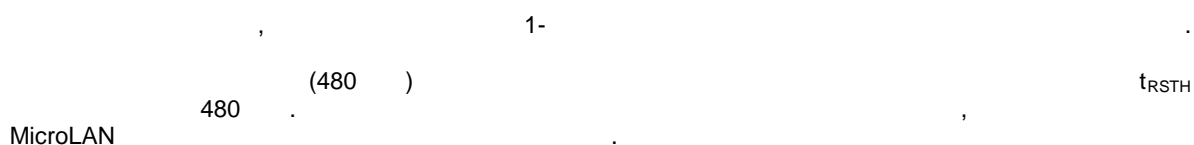
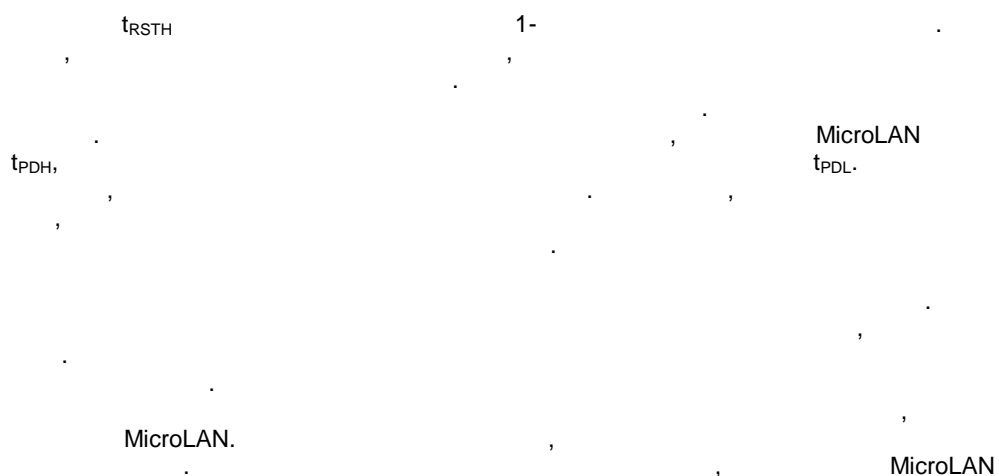
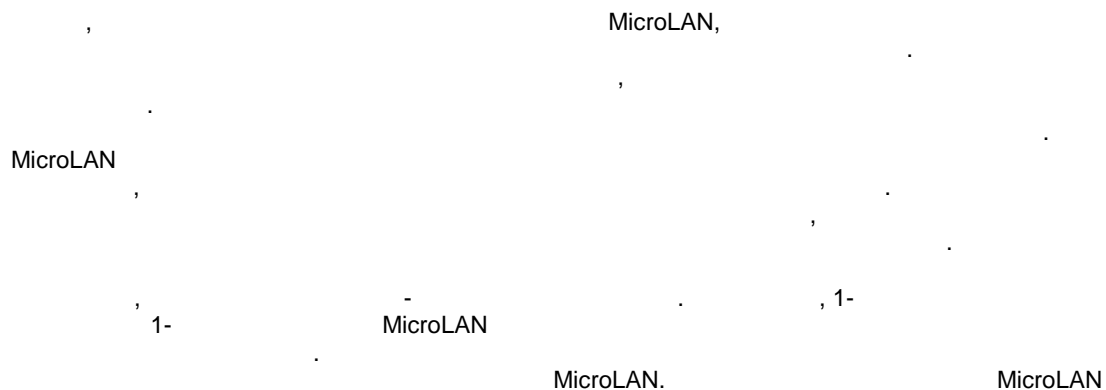


Рис. 4. Импульс сброса и обнаружения присутствия



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Dallas Semiconductor

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DS1990A

DS1990A,

DS1990A,

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	33h  0Fh CCh	DS1990A      55h  F0h   ( MicroLAN )
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( ) , DS1820, DS1920 DS2405, DS2407, ( DS1991). DS1991 DS1991 . DS1991 ,

2.

	F0h	
	A5h	
	66h	DS1991.
	0Fh	32
	96h	DS1991.
	AAh	
	69h	DS1991.
	55h	3-
	3Ch	DS1991.
		8-
	99h	DS1991.
	5Ah	DS1991.
	0Fh	
	55h	
	AAh	

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